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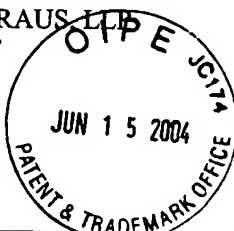
501.38451CC2

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020457 7590 03/15/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/631,793	08/01/2003	Kumiko Takikawa	501.38451CC2	1674

TITLE OF INVENTION: ~~SEMICONDUCTOR INTEGRATED CIRCUIT~~ PIN LAYOUT OF DUAL BAND RECEIVER WITH TWO INPUT PADS/PINS RESTRICTED TO A SINGLE SIDE OF A FOUR-SIDED PACKAGE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/15/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
DICKEY, THOMAS L	2826	257-692000

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2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

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(A) NAME OF ASSIGNEE

Hitachi, Ltd.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) Larry N. Anagnos (Date) June 15, 2004
Larry N. Anagnos # 82,397

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